

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10628074
Filing Date	2003-07-25
First Named Inventor	Moacanin et al.
Art Unit	2858 2117
Examiner Name	BRITT, CYNTHIA H.
Attorney Docket Number	FULCP010

/CB/	20	5732233		1998-03-24	Klim et al.	
/CB/	21	5889919 5889.979		1999-03-30	Miller, Jr. et al.	

If you wish to add additional U.S. Patent citation information please click the Add button.

Add

U.S. PATENT APPLICATION PUBLICATIONS

Remove

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Published Application citation information please click the Add button.

Add

FOREIGN PATENT DOCUMENTS

Remove

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ² j	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
/CB/	1	WO9207361	WO		1992-04-30			<input type="checkbox"/>

If you wish to add additional Foreign Patent Document citation information please click the Add button.

Add

NON-PATENT LITERATURE DOCUMENTS

Remove

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
/CB/	1	VENKAT et al., "Timing Verification of Dynamic Circuits", May 1, 1995, IEEE 1995 Custom Integrated Circuits Conference.	<input type="checkbox"/>
/CB/	2	WILSON, "Fulcrum IC heats asynchronous design debate", August 20, 2002, http://www.fulcrummicro.com/press/article_eeTimes_08-20-02.shtml .	<input type="checkbox"/>

UR
6-8-7